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REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Freeman *et al.* Conf. #: 1211
Serial No.: 10/604,212 Art Unit: 2815
Filed: 07/01/2003 Dkt. #: FIS920030098US1 (IBMF-0016)
Title: BIPOLAR TRANSISTOR SELF-ALIGNMENT WITH RAISED
EXTRINSIC BASE EXTENSION
AND METHODS OF FORMING
SAME

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AFTER FINAL AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS

In response to the Office Action of April 28, 2005, please amend the above-referenced patent application as follows.

10/604,212

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